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Application Number 10/767,540

Filing Date 01-29-2004

First Named Inventor Dwarkanath

Art Unit 2838

Examiner Name Behm, Harry Raymond

Attorney Docket Number ENP-003

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	1	BARRADO, A., et al., "New DC/DC Converter with Low Output Voltage and Fast Transient Response," Proceedings of the IEEE Applied Power Electronics Conference, 2003, pp. 432-437, IEEE, Los Alamitos, CA.	
	2	GODER, D., et al., "V ² Architecture Provides Ultra-Fast Transient Response in Switch Mode Power Supplies," Proceedings of HFPC Power Conversion, 1996, pp. 414-420.	
	3	"Linear Technology: LTC3736-1: Dual 2-Phase, No R _{SENSE} TM , Synchronous Controller with Spread Spectrum," 2004, 28 pp., Linear Technology Corporation, Milpitas, CA.	
	4	PATELLA, B.J., et al., "High-Frequency Digital Controller IC for DC/DC Converters," IEEE Proceedings of the Applied Power Electronics Conference, March 10, 2002, 7 pp., IEEE, Los Alamitos, CA.	
	5	PETERCHEV, A.V., et al., "Quantization Resolution and Limit Cycling in Digitally Controlled PWM Converters," IEEE Transactions on Power Electronics, January 2003, pp. 301-303, Vol. 18, No. 1, IEEE, Los Alamitos, CA.	
	6	REDL, R., et al., "Optimizing the Load Transient Response of the Buck Converter," Proceedings of the IEEE Applied Power Electronics Conference, 1998, pp. 170-176, IEEE, Los Alamitos, CA.	
	7	SCHONEMAN, G.K., et al., "Output Impedance Considerations for Switching Regulators with Current-Injected Control," Proceedings of the 18th Annual IEEE Power Electronics Specialists Conference, June 1987, pp. 324-335, IEEE, Los Alamitos, CA.	
	8	SOTO, A., et al. "Analysis of the Buck Converter for Scaling the Supply Voltage of Digital Circuits," Proceedings of the IEEE Applied Power Electronics Conference, 2003, pp. 711-717, IEEE, Los Alamitos, CA.	
	9	SOTO, A., et al., "Design Methodology for Dynamic Voltage Scaling in the Buck Converter," Proceedings of the IEEE Applied Power Electronics Conference, 2005, pp. 263-269, IEEE, Los Alamitos, CA.	
	10	"TPS40100: Midrange Input Synchronous Buck Controller with Advanced Sequencing and Output Margining," May 2005, 37 pp., Texas Instruments Incorporated, Dallas, TX.	

Examiner Signature	Date Considered
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*Applicant's unique citation designation number (optional). "Applicant is to place a check mark here if English language Translation is attached.

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